

GENERATING PULSES FOR RESETTING INTEGRATED CIRCUITS

Abstract of the Disclosure

An integrated circuit may be initialized by
5 determining that the supply voltage is ramping up and
resetting logic in the integrated circuit to a
predetermined initial state using a reset signal. After
the logic is determined to be in its initial state and the
supply voltage is established, the reset signal may be
10 latched off. A pulse generator includes circuits that
prevent the pulse generator from being reactivated until
after a power cycle.